

Ref: ICSII-SSIP04/FEB25/09

Date: February 27, 2025

Event Report

Event: Minimum Viable Product with SSIP

Type: Seminar

Time: 3:00 PM – 4:00 PM

Venue: LH30, Indus University

Speaker:

Prof. Shaswat Padalia, Asst. Dean R&D, Indus University

Total Participants: 89

Overview

The seminar “**Minimum Viable Product with SSIP**” was conducted to guide student innovators and early-stage founders on building, validating, and refining an MVP aligned with **SSIP expectations**. The session emphasized practical MVP design, validation metrics, and the transition from idea to PoC readiness.

Key Highlights

- **MVP Fundamentals**
 - Defining MVP scope and avoiding overengineering
 - Aligning MVP features with core problem statements
- **Validation Before Scale**
 - User feedback loops, pilots, and early traction indicators
 - Measuring learnings vs. features delivered
- **SSIP Alignment**
 - Mapping MVP outcomes to SSIP evaluation criteria
 - Readiness levels, documentation, and milestone planning
- **IPR & MVP**
 - When to consider provisional filing alongside MVP development
 - Protecting novelty while testing the market
- **Case Insights**
 - Examples of MVPs that progressed to incubation and funding under ICSII

Outcomes

- Participants gained clarity on building **lean, testable MVPs**
- Better understanding of **data-backed validation** for SSIP pitches
- Identification of next steps to strengthen PoC readiness

Conclusion

The session provided a clear, actionable roadmap for founders to convert ideas into validated MVPs using SSIP support. Prof. Padalia highlighted that a focused MVP, backed by evidence, accelerates both evaluation success and market entry.

Organized by: ICSII, Indus University

Duration: 1 Hour

Mode: Offline Seminar

